

# Yasod Ginige

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## SUMMARY AND RESEARCH INTERESTS

A highly motivated, individual who is passionate and willing to learn new concepts to solve problems. Currently, pursue a PhD in computer science at the University of Sydney. My fields of interest are **Machine Learning**, **Computer Networks and Security**, and **Computer Vision**.

## EDUCATION

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|-----------------------|---|
| Jan. 2024 - Present   | <b>UNIVERSITY OF SYDNEY, Australia</b> <ul style="list-style-type: none"><li>Ph.D. Candidate in the School of Computer Science.</li><li>Title : AI Enhanced Cybersecurity for Red Teaming and Blue Teaming Tasks.</li><li>Build autonomous cybersecurity tools integrating AI technologies with security tools. The aim is to cater the skill shortage in security industry and increase the usability of security tools for the general public.</li><li>Finalist in the <b>Google Student Fellowship</b>. Was one of the two nominees of the University of Sydney.</li></ul> |
| Nov. 2018 - Jul 2023  | <b>UNIVERSITY OF MORATUWA, Sri Lanka</b> <ul style="list-style-type: none"><li>BSc. Engineering (Honors), specialized in Electronic and Telecommunication Engineering.</li><li>Cumulative GPA : 4.05/4.20 (<i>First Class Honors</i>).</li><li>Was nominated for the Most Outstanding Graduate of the Year.</li></ul>   |
| Jul. 2015 - Aug. 2017 | <b>RICHMOND COLLEGE - GALLE, Sri Lanka</b> <ul style="list-style-type: none"><li>The most outstanding student of the year 2017.</li><li>National Rank : 8<sup>th</sup> in the G.C.E. Advanced Level Examination (from ~20,000 candidates).</li></ul>  |

## PROFESSIONAL EXPERIENCE

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|------------------------|--|
| Aug. 2024<br>Present   | <b>CASUAL ACADEMIC</b><br><b>University of Sydney, Australia</b> <ul style="list-style-type: none"><li>Conduct tutorials for the COMP5618, CSEC3616, CSEC5616, and INFO3616 Cybersecurity Engineering courses.</li><li>Conduct tutorials for the INFO6007 Project Management in IT course.</li></ul>   |
| Apr. 2024<br>Dec 2024  | <b>CASUAL ADMINISTRATIVE OFFICER - CYBER EVENT ORGANIZATION</b><br><b>University of Sydney, Australia</b> <ul style="list-style-type: none"><li>Conducted cybersecurity workshops for engineering students. Explained practical applications of theories.</li></ul>  |
| Jul. 2023<br>Dec. 2023 | <b>SOFTWARE ENGINEER</b><br><b>Axiata Digital Labs (Pvt) Ltd, Sri Lanka</b> <ul style="list-style-type: none"><li>Worked on a project to build a low latency, high throughput API manager using C++ and Java. Used concepts such as OOP, lambda functions, api, multi-threaded coding, and used relevant tools.</li></ul>  |
| Dec. 2021<br>Sep. 2022 | <b>STUDENT RESEARCH AFFILIATE</b><br><b>University of Sydney, Australia</b> <ul style="list-style-type: none"><li>Worked on two research projects on ML based network security. Studied and researched on traffic fingerprinting attacks, membership inference attacks, and model quantization effects.</li><li>Completed and submitted two papers.</li></ul>  |
| Jan. 2022<br>Jul. 2022 | <b>VISITING RESEARCHER</b><br><b>Monash University, Australia</b> <ul style="list-style-type: none"><li>Worked on a project based on developing a DNA alphabet to store data using the Nanopore technology.</li><li>Used first-order filters for signal processing, statistical models like Markov models for classification.</li><li>Developed machine learning models to identify DNA patterns of the alphabet with 96% accuracy with higher throughput.</li></ul> |
| Oct. 2022<br>Jul. 2023 | <b>VISITING INSTRUCTOR</b><br><b>University of Moratuwa, Sri Lanka</b> <ul style="list-style-type: none"><li>Conducted lab sessions for the "EN3143 : Electronic Control Systems" module. Explained theories and their applications.</li></ul>   |

## PUBLICATIONS

- Ginige Y., Akila N., Jain S., Seneviratne S. AutoPentester : An LLM Agent-based Framework for Automated Pentesting - Accepted for IEEE TrustCom 2025 (A).

- **Ginige Y.**, Silva B., Dahanayaka T., Seneviratne S. TrafficLLM : LLMs for Improved Open-Set Encrypted Traffic Analysis - Accepted for Computer Networks Journal (Q1).
- Dahanayaka T., **Ginige Y.**, Huang Y., Jourjon G., & Seneviratne S. (2023). Robust open-set classification for encrypted traffic fingerprinting. Computer Networks (Q1), 236, 109991. [pdf]
- Karunanayake, N., Silva, B., **Ginige Y.**, Seneviratne S., Chawla S. Quantifying and Exploiting Adversarial Vulnerability : Gradient-Based Input Pre-Filtering for Enhanced Performance in Black-Box Attacks - ACM Transactions on Privacy and Security Journal (Q1). [pdf]
- **Ginige Y.**, Dahanayaka T., Seneviratne S. TrafficGPT : An LLM Approach for Open-Set Encrypted Traffic Classification - Asian Internet Engineering Conference. [pdf]
- **Ginige Y.**, Gunasekara R., Hevawithara D., Ariyaratne M., Rodrigo R., Jayasekara P. Object Tracking, Reidentification and Activity Detection for Maritime Surveillance - Under review at the Machine Learning and Cybernetics Journal (Q2).[video]
- Tang L., Bogahawatta N., **Ginige Y.**, Xu J., Sun S., Ranathunga S., Seneviratne S. A Framework to Assess Multilingual Vulnerabilities of LLMs - International World Wide Web (WWW) Conference (A\*).

## ARTICLES

- E-Carrier magazine : Official magazine of the Department of Electronic and Telecommunication Engineering  
Published two articles, namely “[Silicon to DNA](#)” and “[Website Traffic Fingerprinting](#)”.

## RESEARCH EXPERIENCE

### PHD RESEARCH

#### LLM Agents Based Autonomous Penetration Testing

Advisors : Dr. Suranga Seneviratne and Prof. Aruna Seneviratne

University of Sydney, Australia

2024 - Present

- Developed an automated pentesting tool based on LLMs and ML techniques to address the skill shortage in cybersecurity.
- Used a multi-agent-based system with a hierarchical RAG architecture to solve the dynamic attack environment mapping and strategy identification in the pentesting processes.
- Used LLMs and RAG architectures along with techniques such as chain-of-thought reasoning, reinforcement learning, and in-context learning. Used OOP principles in coding and frameworks like PyTorch, LangChain, OpenAI, and Transformers.

### INTERNSHIP RESEARCH

#### Robust Open Set Classification for Encrypted Traffic Fingerprinting [code]

Advisors : Dr. Suranga Seneviratne and Dr. Thilini Dahanayake

University of Sydney, Australia

2021 - 2022

- Developed a novel network traffic classification method that can be deployed in resource constrained network devices using deep learning and statistical methods.
- The method could quantize classifier models to a 4-bit state with only 4% of performance degradation.

#### Quantifying and Exploiting Adversarial Vulnerabilities using Gradient-Based Input Pre-Filtering

Advisors : Dr. Suranga Seneviratne and Dr. Sanjay Chawla

2022

- Proposed a novel adversarial perturbation technique to optimize adversarial inputs for trained machine learning models using a novel technique called zero gradients.
- The method outperformed the state-of-the-art. We proved the transferability of our method between ML models using different datasets and shadow models.

### HONORS RESEARCH

#### Object Tracking, Reidentification and Activity Detection for Maritime Surveillance [code]

Advisors : Dr. Peshala Jayasekara and Dr. Ranga Rodrigo

University of Moratuwa, Sri Lanka

2022-2023

- Developed a thermal-based maritime surveillance system with real-time object tracking, suspicious activity detection, and viewpoint-invariant vessel re-identification, integrated into an interactive GUI for continuous 24x7 monitoring and alerting.
- The novel re-identification algorithm can re-identify vessels from different viewpoints, paying attention to even minor features of the vessel's shape. The work is under review.
- The activity detection algorithm identifies suspicious activities like human trafficking by processing the video feed in real time.

### EXTERNAL RESEARCH

#### Encoding and Decoding Data into DNA Using an Alphabet of DNA Sequences

Advisors : Prof. Emanuele Viterbo and Dr. Viduranga Wijekoon

Monash University, Australia

2022

- Involved in a next generation technological research where we store data in high density DNA patterns.
- Researched on deep learning approaches to classify nanopore signals (small electric signals) corresponding to a given alphabet. Used CNN and LSTM architectures and obtained 95% accuracy.
- Used first order filters and Markov chains to optimize results for real world data.

- The project aims to identify anomalies of a multi-robot system using IMU, camera, and Lidar sensor data.
- Implemented an auto-encoder model to identify anomalies in IMU data of an autonomous robot.
- Used a GAN model to reconstruct the next frame of a frame sequence, thereby identifying anomalies in the camera sensor data.

## OTHER PROJECTS

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### SMART BREADBOARD : TO FACILITATE REMOTE LAB EXPERIMENTS.

[Skills : Electronics, Arduino, IoT, Python]

Designed a smart breadboard using a MOSFET matrix to support virtual experiments during pandemic periods. Connections between columns and rows are made by switching on MOSFETs according to a user input. Users design circuits through the GUI, which are automatically implemented on the smart breadboard by switching MOSFET switches. The project won the IEEE CAS Student Design Competition 2020/21 in Sri Lanka and was selected for the IEEE Region 10 finals. [\[video\]](#)

### ELDERBOT : ELDERLY CARE ELECTRONIC PRODUCT.

[Skills : Electronics, Arduino, HTML, Solidworks]

Designed an electronic product to assist elderly people in a fall or an emergency situation by sending alarm messages. Used an MPU6050 sensor, NodeMCU, and MQTT, and HTTP protocols. Developed software and a UI for user login and profile management. [\[video\]](#)

### AUTONOMOUS MOBILE AND ASSISTING ROBOT FOR SLRC 2018. [\[CODE\]](#)

[Skills : Electronics, Arduino, Solidworks]

Designed and developed two robots. A mobile autonomous robot that follows lines and dashed lines, solves a line maze (using the DFS algorithm), collects, detects, sorts by color, and unloads coins. It communicates with an immobile assisting robot, which removes obstacles from its path.

### IMAGE DOWNSAMPLING PROCESSOR DESIGN AND FPGA IMPLEMENTATION. [\[CODE\]](#)

[Skills : Verilog, FPGA]

Designed and implemented a RISC processor to downsample an image on the Altera DE2-115 FPGA board.

### FILE COMPRESSION AND DECOMPRESSION ALGORITHM [\[CODE\]](#)

[Skills : C++]

Implement both code compression and decompression using C++ for a given input file. A customized rule set was used for the compression and decompression tasks.

## HONORS AND AWARDS

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### Honorable Mention Award : International Physics Olympiad

2018

- Organized by the International Physics Olympiad Board

### World Champions : IEEE ICAS 2021 Challenge (Montreal, Canada) :

2021

- Organized by IEEE Signal Processing Society

### World Champions : IEEE IES Student Branch Chapter Competition :

2022

- Organized by IEEE Industrial Electronics Society

### World Runner-up : IEEE International Humanitarian Technology Video Competition :

2022

- Organized by IEEE Canada

### Represented Sri Lanka in the Asian Physics Olympiad

2018

- Organized by the Physics Olympiad Committee of Vietnam

### NSW Connectivity Innovation Network(CIN) PhD scholarship

2023

- Awarded by Connectivity Innovation Network, NSW, Australia

### Champions : 2nd International Energy and Electricity Market Business Decision Competition

2020

- Organized by the Shanghai University, China

### Nominee for the Most Outstanding Graduate of the Year 2023

2023

- University of Moratuwa, Sri Lanka

### Gold Medalist : Sri Lankan Physics Olympiad 2018

2018

- Organized by the Institute of Physics, Sri Lanka

### Dean's List in Semesters 1, 2, 4, 5, 6, 7, and 8

2018 - 2023

- Faculty of Engineering, University of Moratuwa

### Darrel Medal : The Most Outstanding Advanced Level Student Award

2017

- Awarded by Richmond College, Galle, Sri Lanka

### Small Medal : Best Student in Mathematics

2015

- Awarded by Richmond College, Galle, Sri Lanka

### School Colors for Cricket

2013

- Awarded by Richmond College, Galle, Sri Lanka

## TECHNICAL SKILLS

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PROGRAMMING LANGUAGES	Python, C/C++, Java, Verilog
LIBRARIES	Tensorflow, Pytorch, NumPy, Keras, Scikit-learn, Pandas, LangChain
SOFTWARE TOOLS	MATLAB, Altium, SOLIDWORKS, Spring Boot, Git, LaTeX, SonarQube, NodeRed
HARDWARE	Atmel AVR, Altera DE2, Raspberry Pi, NodeMCU

## COURSES AND CERTIFICATION

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Jun. 2020	<b>IMPERIAL COLLEGE, LONDON, COURSERA</b> <ul style="list-style-type: none"><li>➤ Mathematics for Machine Learning-Linear Algebra</li></ul>
Aug. 2020	<b>DEEPLARNING.AI</b> <ul style="list-style-type: none"><li>➤ Deep Learning Specialization – 5 Courses</li></ul>
Aug. 2023 - Present	<b>EC COUNCIL</b> <ul style="list-style-type: none"><li>➤ Certified Ethical Hacker Certification</li></ul>

## VOLUNTEERING AND LEADERSHIP SKILLS

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### IEEE Industrial Electronics Society - Student Branch Chapter of University of Moratuwa

- Chairman (2022-2023) and Vice Chairman (2021-2022)
- Provided leadership for a 150 member base to organize workshops, seminars, competitions, and social events.
- Emerged as “The best IES student branch chapter” in the worldwide competition in 2022.
- Won the “Emerging Student Branch Chapter” award in the IEEE Sri Lanka section awards – 2022.

### Young Professionals Coordinator : Institute of Engineers Sri Lanka (IESL) NSW Chapter

- Main organizer HDR student gathering at Universities in Sydney.
- Organized a traditional event to celebrate the Sinhala and Hindu New Year.

### HDR Student Representative : School of Computer Science, University of Sydney.

- Organized technical and non-technical HDR events to build the network and improve social skills in HDR students.

### Mathematics Society, University of Moratuwa

- Chairman in 2020-2021
- Provided leadership for a 300 member base to organize seminars, workshops and competitions.

### Co-chair of the Gold Chasers project - Rotaract Cub, University of Moratuwa

- Organized practical and tutorial sessions for the Sri Lankan Physics Olympiad team, 2019.
- The team obtained 2 bronze medals and one honorable mention in the International Physics Olympiad 2019.

### Member of the Cricket Team, Richmond College Galle

- Won the all island championship in 2013 and semi-finalists in 2012.

## REFERENCES

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**Dr. Suranga Seneviratne**  
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School of Computer Science  
University of Sydney  
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